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Application Number	TBD														
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First Named Inventor	Chee Kiang Yew, et al.														
Group Art Unit	TBD														
Examiner Name	TBD														
Sheet	1	of	2	Attorney Docket No.	TI-26239.1										

U.S. PATENT DOCUMENTS						
Exam. Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
PL	AA	5,061,509		Nalto, et al.	10/29/1991	
PL	AB	5,353,498		Fillion, et al.	10/11/1994	
PL	AC	5,739,585		Akram, et al.	04/14/1998	
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PC	AH	6,242,283 B1		Lo, et al.	06/05/2001	
	AI					
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FOREIGN PATENT DOCUMENTS							
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*PHAT X. CAO**2/12/04*

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**INFORMATION DISCLOSURE
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Sheet 2 of 2 Attorney Docket No. TI-26239.1

Complete If Known

Application Number	TBD
Filing Date	Herewith
First Named Inventor	Chee Kiang Yew, et al.
Group Art Unit	TBD
Examiner Name	TBD

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PC	CA	"Silicon Processing for the VLSI Era", Stanley Wolf, Ph.D., et al., Volume 1: Process Technology, Lattice Press, Sunset Beach, CA, 1986, pp. 547-551	
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